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01/17/02

PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10051701	01/17/2002	349	43	2871	DUNN, JAE THOI

**\*\*APPLICANTS:** Jeon Jin.

**\*\*CONTINUING DATA VERIFIED:**

**\*\* FOREIGN APPLICATIONS VERIFIED:**  
REPUBLIC OF KOREA 2001 44295 07/24/2001

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	8054-4 (LW7020US)
Verified and Acknowledged Examiners's initials	
TITLE : Thin film transistor substrate for liquid crystal display panel and manufacturing method thereof	

U.S. DEPT. OF COMMERCE PAT. & TM. PTO 436 (Rev. 12-95)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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